

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Yi Ding

Assignee:

ProMOS Technologies, Inc.

Title:

Nonvolatile Memory Cells With Buried Channel Transistors

Application No.:

10/632,155

Filing Date:

July 30, 2003

Examiner:

Nhu, David

Group Art Unit: 2818

Docket No.:

M-15222 US

San Jose, California November 3, 2004

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR § 1.97(b)

Dear Sir:

Pursuant to 37 CFR § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying form PTO-1449 are called to the attention of the Examiner for the above patent application. Copies of these documents are enclosed except for United States Patents and United States Published Patent Applications.

Citation of these documents shall not be construed as:

- 1. an admission that the documents are necessarily prior art with respect to the instant invention;
 - 2. a representation that a search has been made, other than as described above; or
- an admission that the information cited herein is, or is considered to be material to patentability as defined in § 1.56(b).

Please charge the fee of \$180.00 to Deposit Account No. 50-2257 for this Information Disclosure Statement and any other fees that may be required. This paper is being submitted in duplicate.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on November 3, 2004.

Respectfully submitted,

and Shenker Michael Shenker

Attorney for Applicant(s)

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U.S. Department of Commerce, Patent and Trademark Office					Atty Docket No.		Serial No.		
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ه بر روز (Use several sheets if necessary)					Yi Ding				
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-			U.S. Pate	ent Documents				True n	
*Examiner Initial	Document Number		Date	Name	Class	Class Subclass		Filing Date If Appropriate	
	AA	6,265,739	Jul. 2001	Yaegashi et al					
	AB	6,747,310	Jun. 2004	Fan et al.					
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